

AMENDMENTS TO THE CLAIMS

1-6. (Canceled)

7. (Previously presented) A display device comprising:

a level shifter configured to change an amplitude of gradation data from a first voltage range to a second voltage range, amplified gradation data being said gradation data at said second voltage range,

wherein output data during a quiescent period is dummy data, said output data during a period other than said quiescent period being said amplified gradation data.

8. (Previously presented) The display device according to claim 7, wherein a maximum value of said first voltage range is lower than a maximum value of said second voltage range.

9. (Previously presented) The display device according to claim 7, wherein said first voltage range is 0 volts to 3 volts and said second voltage range is 0 volts to 6 volts.

10. (Previously presented) The display device according to claim 7, wherein said quiescent period is during which said gradation data is held at a constant logical level for a constant period at a constant cycle.

11. (Previously presented) The display device according to claim 10, wherein said dummy data has a logical level opposite to said constant logical level.

12. (Previously presented) The display device according to claim 10, wherein said quiescent period is a horizontal blanking period.

13. (Previously presented) The display device according to claim 10, wherein said quiescent period is a vertical blanking period.

14. (Previously presented) The display device according to claim 10, wherein said gradation data is video data.

15. (Previously presented) The display device according to claim 7, wherein said amplitude of the output data is changed from said second voltage range to said first voltage range, resultant gradation data being said output data at said first voltage range.

16. (Previously presented) The display device according to claim 15, wherein said resultant gradation includes said output data that has been latched on a rising edge of a sampling pulse.

17. (Previously presented) The display device according to claim 16, wherein said resultant gradation includes said output data that has been latched on a falling edge of a sampling pulse.

18. (Previously presented) The display device according to claim 15, wherein a horizontal driving circuit converts said resultant gradation data into analog signals.

19. (Previously presented) The display device according to claim 18, wherein a vertical driving circuit sequentially selects pixels through gate lines, said pixels selected through said gate lines being driven by said analog signals.

20. (Previously presented) The display device according to claim 19, wherein said pixels are arranged in a matrix form.

21. (Currently amended) The display device according to claim 15, ~~7~~, wherein an active device for processing said resultant gradation data is formed by low-temperature polysilicon.

22. (Currently amended) The display device according to claim 15, ~~7~~, wherein an active device for processing said resultant gradation data is formed by continuous grain silicon.